

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made.

Canada

2,045,907	B. Gross, G. B. Anderson, J. W. Marlin, and V. D. Tucker	A method for storing and retrieving annotations and reactions in final form documents
2,093,450	D. Mooney, I. R. Ameline, and K. A. Stoodley	Method and procedure call mechanism for calling 16-bit functions from 32-bit functions
2,097,541	G. O'Farrell William	Concurrency control through a class library in object oriented technology
2,097,542	G. O'Farrell William	Linkage for accessing data objects in the stack
2,109,682	L. Hartley	Multiple bus interface
2,115,463	G. O'Farrell William and I. Kalas	Mechanism and method for allocating active objects on a processor stack
2,115,464	E. Arjomandi and G. O'Farrell William	Concurrent processing in object oriented parallel and near parallel systems
2,132,764	B. J. Blainey	Method and system of generating combined storage references
2,135,508	A. M. Aulicino and R. J. Lyn	Method for forming solder balls on a semiconductor substrate

France

638,801	O. Collet-Beillon	Method of inspecting the array of balls of an integrated circuit module
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Germany

59,109,081.3	H. Eberle, K. Geihs, R. Heite, and U. Hollberg	Access control in computer networks
59,503,545.0	W. Schawer	Computerized conversion of spreadsheet tables
59,503,705.4	U. Maier and W. Ruppert	Written character recognition device
69,227,881.8	U. Rackow and U. Schwall	System for text translation

Japan

1,026,028	T. Takahashi	Industrial design of IC card reader
2,139,015	K. Ikarugi, K. Noda, T. Fukuzawa, T. Hisano, and T. Morita	Analyzer
2,821,840	N. Uramoto	Mechanism of word-sense disambiguation with distinctive features from examples
2,823,106	A. Ohkubo, K. Yamamoto, M. Kinbara, M. Tsuchiya, and O. Inoue	Generalized mail gateway with mail API to connect multiple different office systems
2,826,252	T. E. Semba	A signal calibration method of an optical disk drive
2,828,221	Y. Taira	Device for efficient frequency conversion of lasers
2,837,376	T. Fujino, T. Takahashi, Y. Natsuyama, and Y. Uchiyama	Super hinge with large size LCD of A4 PC

2,837,377	K. Itoh, T. Tsuchiya, T. Yoshida, and Y. Ichimura	Cross surface treatment at top of bonding tool
2,855,409	T. E. Nasukawa	A method of parsing ill-formed sentences by using information extracted from well-formed sentences in a same text
2,856,621	H. Asano, H. Nijima, T. A. Toyooka, and Y. Sakaue	Sector maintenance for SSD with block-erase flash EEPROM
2,856,700	D. Albrecht, S. Tsuda, and T. Nakamoto	Stepped clamp
2,859,559	T. Sakairi	A clipboard mechanism for shared applications
Switzerland		
522,224	M. Heddes and R. P. Luijten	High speed buffer management
689,748	E. Ruetsche and M. Kaiserswerth	Method and apparatus for extracting connection information from protocol headers
722,574	B. Hecht, D. W. Pohl, H. Heinzelmann, and L. Novotny	Near-field optical microscope
784,895	I. Iliadis	Flow control method and apparatus for cell-based communication networks
United Kingdom		
709,443	F. J. Martin and J. S. Beeteson	Method and apparatus for bonding
795,242	J. R. Lewis	Providing routing tables using a directory service
797,796	B. D. Gladwyn, P. S. Adams, and V. J. Singleton	Method for the indication of updating process for a remote display
803,161	J. S. Beeteson	Cathode ray tube display apparatus
815,540	J. Maltby	Interframe coding method and apparatus
2,075,780	J. Tierney	18" flat panel design
2,075,781	J. Tierney	15" flat panel monitor
2,304,986	A. R. Knox and J. S. Beeteson	Electron source
2,304,987	A. R. Knox and J. S. Beeteson	Display device
2,318,209	A. R. Knox, J. S. Beeteson, and S. C. Kerigan	Display system
2,319,102	J. M. Flenley, P. J. Atkin, and S. Currie	A security module for a transaction processing system
2,319,640	J. M. Flenley, P. J. Atkin, and S. Currie	Transaction processing system
2,319,641	J. M. Flenley	Secure variable storage for Internet applications
United States		
5,794,248	J. L. Howe III and K. R. Blackman	A method for representing non-object-oriented datastores using a collection of object collections data model
5,794,454	D. McClafferty, D. R. Barringer, D. R. Horvath, R. G. Kemink, R. R. Schmidt, and W. S. Harris	Cooling device for hard to access non-coplanar circuit chips
5,794,687	L. H. Webster, L. S. Mok, M. S. Mansuria, and V. Kamath	Forced air cooling apparatus for semiconductor chips
5,795,087	D. C. Brower, J. D. Thomas, and R. Myers	Pivoting roller for skewless document feed
5,795,217	A. Nenadic, A. Piciacchio, D. C. Long, M. LaPlante, and T. Lombardi	Stressed burnisher
5,795,520	B. D. Sewing and L. Scalia	Method for in-situ green sheet slitting
5,795,653	J. E. Yehoda and J. J. Cuomo	Method for polishing a diamond or carbon nitride film by reaction with oxygen transported to the film through a superionic conductor in contact with the film
5,795,685	B. J. Lin, L. Liebmann, and M. O. Neisser	Simple repair method for phase shifting masks
5,795,701	J. T. Fahey, K. M. Welsh, R. Sooriyakumaran, W. E. Conley, and W. M. Moreau	Making of microlithographic structures with an underlayer film containing a thermolyzed azide compound
5,795,819	R. M. Geffken, R. R. Uttecht, and W. T. Motsiff	Integrated pad and fuse structure for planar copper metallurgy
5,795,826	J. P. Gambino, L. A. Nesbit, and M. Jaso	Method of chemically mechanically polishing an electronic component
5,795,830	C. Kaanta and J. E. Cronin	Reducing pitch with continuously adjusting line and space dimensions
5,796,050	J. S. Campbell	Flexible board having adhesive in surface channels
5,796,121	S. M. Gates	Thin film transistor fabricated on plastic substrates
5,796,166	A. Grill, C. Cabral, C. Jahnes, P. D. Agnello, R. A. Roy, and T. J. Licata	Tasin oxygen diffusion barrier in multilayer structures

5,796,168	M. Datta, M. B. Pike, R. Shenoy, and T. Kanarsky	Metallic interconnect pad and integrated circuit structure using same with reduced undercut
5,796,169	C. L. Reynolds, C. N. Ramirez, G. J. Robbins, R. C. Dockerty, R. M. Fraga, and S. K. Ray	Structurally reinforced ball grid array semiconductor packages and systems
5,796,270	G. Giacalone, J. A. Fifield, and P. Jenkins	Self-timed driver circuit
5,796,271	M. Kumar	Memory array having redundant word line
5,796,383	S. D. Redpath and S. F. Henshaw	Method and system for presenting contents of a container object within a graphical user interface in a data processing system
5,796,386	J. Rossignac, J. P. Menon, J. S. Lipscomb, and R. H. Wolfe	Precise calibration procedure for sensor-based view point control system
5,796,452	M. V. Pierson	Simplified wiring escape technique for tiled display
5,796,509	F. E. Doany and R. R. Troutman	Thin film frontlighting and backlighting for spatial light modulators
5,796,561	D. Mauri	Self biased spin valve sensor
5,796,573	D. E. Kotecki and W. H. Ma	Overhanging separator for self-defining stacked capacitor
5,796,581	L. S. Mok	Rotational joint for hinged heat pipe cooling of a computer
5,796,587	J. M. Lauffer and K. I. Papathomas	Printed circuit board with embedded decoupling capacitance and method for producing same
5,796,591	G. J. Gaudenzi, H. M. Dalal, and K. Fallon	Direct chip attach circuit card
5,796,621	G. Pruesse, P. E. Dudley, and P. T. Gutwin	Circuit delay abstraction tool
5,796,641	N. Tu	System and table-based method for converting binary floating-point numbers to a decimal representation
5,796,662	H. L. Kalter and J. E. Barth	Integrated circuit chip with a wide I/O memory array and redundant data lines
5,796,719	L. Georgiadis, R. Guerin, S. Varma, and V. Peris	Traffic flow regulation to guarantee end-to-end delay in packet switched networks
5,796,727	C. G. Harrison and D. Jaepel	Wide-area wireless LAN access
5,796,745	G. S. Koch, J. Connor, L. Ternullo, and R. D. Adams	Memory array built-in self test circuit for testing multi-port memory arrays
5,796,751	S. Kundu	Technique for sorting high frequency integrated circuits
5,796,758	D. S. Levitan	A self-checking content addressable memory and method of operation for detecting multiple selected word lines
5,796,827	D. Coppersmith, P. Raghavan, and T. G. Zimmerman	System and method for near-field human-body coupling for encrypted communication with identification cards
5,796,830	C. W. Kaufman, D. B. Johnson, D. R. Safford, M. M. Yung, N. Zunic, P. A. Karger, and S. M. Matyas	Interoperable cryptographic key recovery system
5,796,938	C. A. Klein, F. J. Misewich, and R. G. Emberty	Diagnostic subsystem and method for SCSI interface
5,796,964	B. M. Bass, C. S. Lingafelt, D. R. Henderson, E. H. Ku, J. Kidd, K. P. Heron, and L. B. Reiss	Method for modifying an existing computer bus to enhance system performance
5,796,979	G. L. Guthrie, J. D. Lewis, J. S. Dodson, and R. K. Arimilli	Data processing system having demand based write through cache with enforced ordering
5,796,990	M. Erle, M. Graf, and P. Wohl	Hierarchical fault modeling system and method
5,796,998	A. R. Talcott, D. S. Levitan, J. S. Muhich, and S. W. White	Apparatus and method for instruction fetching in an information handling system
5,796,999	A. Azagury, D. Dolev, G. Gofit, J. Satran, J. G. Ranweiler, and J. M. Marberg	Method and system for selectable consistency level maintenance in a resilient database system
5,797,000	J. Y. Chung, L. P. Zaino, M. Viveros, M. H. Pirahesh, P. G. Selinger, P. P. Bhattacharya, and Y. Wang	Method of performing a parallel relational database query in a multiprocessor environment
5,797,005	A. V. Vakkalagadda, B. E. Naylor, B. J. Patterson, G. S. Denny, J. L. Mansker, J. W. Bahls, J. Y. Tse, K. D. Paffendorf, R. G. Hannan, and S. L. Stooß	Utilization of a coupling facility list structure as a queuing facility
5,797,007	C. R. Erickson and R. H. Sessions	Persistent object storage system with default object encoder/decoder
5,797,011	D. W. Johnston and O. Kroll	Method for controlling the translation of information on a display screen from a source language to a target language
5,797,014	A. Gheith	Method for eliminating intra-module global offset table address computation
5,797,019	C. P. Roth, E. H. Welbon, and F. E. Levine	Method and system for performance monitoring time lengths of disabled interrupts in a processing system
5,797,030	S. E. Hoaby	Output library administrator that provides secure, on-demand output control

5,797,136	G. G. Kiernan and P. Boyer	Optional quantifiers in relational and object-oriented views of database systems
5,797,139	H. Y. Amro	Method, memory and apparatus for designating a file's type
5,798,016	D. Vender, G. S. Oehrlein, M. Haverlag, and Y. Zhang	Apparatus for hot wall reactive ion etching using a dielectric or metallic liner with temperature control to achieve process stability
5,798,050	J. A. Molla and M. A. Gaynes	Process for fabrication of a selectively filled flexible adhesive device for solderless connection of electronic modules to a substrate
5,798,282	C. L. Bertin, E. L. Hedberg, and W. J. Howell	Semiconductor stack structures and fabrication sparing methods utilizing programmable spare circuit
5,798,285	K. M. Fallon, L. H. White, and M. R. Bentlage	Method of making electronic module with multiple solder dams in soldermask window
5,798,469	R. W. Nufer	Non-sintering controlled pattern formation
5,798,525	M. Jung, P. Benizri-Carl, T. van Kessel, and W. Egert	X-ray enhanced SEM critical dimension measurement
5,798,529	A. Wagner	Focused ion beam metrology
5,798,553	J. A. Mandelman, T. Furukawa, and W. R. Tonti	Trench-isolated FET devices, and method for their manufacture
5,798,563	J. S. Kresge, J. W. Wilson, N. B. Feilchenfeld, R. P. Nowak, and S. P. Moore	Polytetrafluoroethylene thin film chip carrier
5,798,754	E. J. Selker, K. Seki, K. Yonemochi, and M. Suzuki	Grip cap for computer control stick
5,798,759	S. S. Dahl	Method and apparatus for mobile device screen reformatting
5,798,890	H. A. Santini, M. L. Williams, R. E. Fontana, and S. R. Hetzler	Disk drive with identical three-element, side-by-side heads on opposite disk surfaces
5,798,909	A. Bhatt, J. A. Knight, S. Desai, S. D. Desai, and T. P. Duffy	Single-tiered organic chip carriers for wire bond-type chips
5,798,918	C. J. Georgiou, E. S. Kirkpatrick, and T. A. Larsen	Performance-temperature optimization by modulating the switching factor of a circuit
5,798,950	J. M. Fitzgerald	Method and apparatus for estimating durations of activities in forming a current system, based on past durations of activities in forming past systems
5,799,146	A. S. Gopal, J. A. Pershing, O. T. Kirby, P. R. Badovinatz, and T. D. Chandra	A communications system involving groups of processors of a distributed computing environment
5,799,149	K. C. Briskey, K. K. Rothaupt, and L. Brenner	System partitioning for massively parallel processors
5,799,170	A. D. Drumm, B. G. Rudolph, and R. L. Kanzelman	Simplified buffer manipulation using standard repowering function
5,799,179	G. M. Silberman and K. Ebcioglu	Handling of exceptions in speculative instructions
5,799,187	G. F. McBrearty	System and method for creating and maintaining a CD ROM client in a computer network
5,799,188	F. L. Rawson III and R. K. Manikundalam	System and method for managing variable weight thread contexts in a multithreaded computer system
5,799,189	C. A. Rogers, F. M. Ring, J. J. Vriezen, L. W. Koser, M. C. Manges, and M. J. Snyder	Computer system with downward compatibility function
5,799,231	A. C. Lowe, B. Pezeshki, and S. M. Gates	Variable index distributed mirror
5,799,292	A. Hekmatpour	Adaptive hypermedia presentation method and system
5,799,300	C. T. Ho, R. Agrawal, and R. Srikant	Method and system for performing range-sum queries on a data cube
5,799,311	J. C. Shafer, M. Mehta, and R. Agrawal	Method and system for generating a decision-tree classifier independent of system memory size
5,799,312	I. Rigoutsos	Three-dimensional affine-invariant hashing defined over any three-dimensional convex domain and producing uniformly-distributed hash keys
5,799,379	G. J. Saxenmeyer, Jr., J. Galvagni, and R. G. Murphy	Method of manufacturing a decoupling capacitor structure
5,800,181	C. F. Ingersoll, G. L. Mack, and P. D. Heinlein	Computer system and method for process safety management
5,800,184	C. Tompkins, E. F. Loperolo, J. M. Sullivan, and L. S. Goldmann	High density electrical interconnect apparatus and method
5,800,626	D. I. Rath, E. Cooper, K. Penner, K. K. Srivastava, and S. L. Cohen	Control of gas content in process liquids for improved megasonic cleaning of semiconductor wafers and microelectronics substrates
5,800,726	J. Cotte, M. Datta, R. Shenoy, and T. Dinan	Selective chemical etching in microelectronics fabrication
5,800,761	A. Vogel, C. Sullivan, J. A. Casey, M. Cohn, M. E. Cropp, and R. J. Sullivan	Method of making an interface layer for stacked lamination sizing and sintering

5,800,858	A. Viehbeck, E. Foster, H. R. Bickford, L. C. Matthew, M. Goldberg, S. L. Tisdale, and V. Markovich	Method for conditioning halogenated polymeric materials and structures fabricated therewith
5,800,874	B. K. Appelt, K. I. Papathomas, M. D. Poliks, R. M. Japp, and W. T. Fotorny	Technique for forming resin-impregnated fiberglass sheets
5,800,963	C. J. Knors, E. Macy, and W. M. Moreau	Polymeric dyes for antireflective coatings
5,801,089	D. Kenney	A method of forming stacked devices
5,801,381	E. Flecha, K. G. Roessler, and R. M. Stowell	Method for protecting a probe tip using active lateral scanning control
5,801,444	L. Krusin-Elbaum, M. O. Aboelfotoh, and Y. C. Sun	Multilevel electronic structures containing copper layer and copper-semiconductor layers
5,801,524	D. W. Boerstler	Voltage controlled current source for low voltage applications
5,801,549	T. Chiu and T. A. Cao	Simultaneous transmission bidirectional repeater and initialization mechanism
5,801,691	S. S. Dahl	Method and apparatus for mobile device screen reformatting utilizing hypertext
5,801,693	J. H. Bailey	A clear extension to a paste command for a clipboard function in a computer system
5,801,697	G. C. Manthuruthil, H. Reddy, and S. Parikh	Method and apparatus for preventing unintentional perusal of computer display information
5,801,698	D. Lection and D. A. Schell	Dynamic information display during busy cycle of a processing system
5,801,699	C. A. Pickover, D. Winarski, J. G. McLean, M. D. Hocker, and N. Keller	Icon aggregation on a graphical user interface
5,801,785	D. T. Crump, J. J. Hurd, S. Pancoast, and T. K. Worthington	Method and system for processing two analog composite video signals
5,801,796	A. C. Lowe	Stacked parallax-free liquid crystal display cell
5,801,894	H. Tzeng and Z. E. Boutaghou	Power-saving high performance data storage system
5,801,896	D. A. Freitas	Method and apparatus for servo pulse detection with high AC coupling and large pulse asymmetry
5,801,903	D. Meyer	Contact recording slider for use in a lubricant containing information storage system
5,801,905	H. M. Tzeng and N. B. Schirle	Actuator arm with cutouts and means for filling or blocking the cutouts
5,801,941	R. L. Bertram	Mobile client computer programmed to establish soft keyboard targeting sensitivity
5,801,984	S. S. Parkin	Magnetic tunnel junction device with ferromagnetic multilayer having fixed magnetic moment
5,802,003	F. R. Keyser, J. A. Iadanza, M. J. Laramie, and R. D. Kilmoyer	System for implementing write, initialization, and reset in a memory array using a single cell write port
5,802,031	A. R. Clark and R. A. Hutchins	Writing system for optical disk
5,802,053	A. L. Morse, D. P. Pozefsky, G. Bollella, K. M. Hogan, S. Sarkar, and V. Kompella	Transport gateway between a native network and a mixed network
5,802,070	F. J. Towler, G. M. Bracer, K. A. Batson, and R. D. Adams	Testing associative memory
5,802,151	B. Bevil, J. W. Sylivant, P. R. Tomaszewski, T. M. Rasmus, and W. T. Kalin	Telephone interface protection circuit and modem using same
5,802,251	E. B. Sherwin, Jr., J. Vizcaino, J. M. Lucassen, and P. S. Cohen	Method and system for reducing perplexity in speech recognition via caller identification
5,802,258	S. Chen	Loosely coupled system environment designed to handle a non-disruptive host connection switch after detection of an error condition or during a host outage or failure
5,802,273	C. P. Roth, E. H. Welbon, F. E. Levine, and R. Moore	Trailing edge analysis
5,802,276	A. Nadalin, G. R. Blakley III, and M. Benantar	Information handling system, method, and article of manufacture including a vault object for encapsulation of object security credentials
5,802,288	K. Ekanadham and R. Mraz	Integrated communications for pipelined computers
5,802,301	A. Dan, D. Sitaram, M. G. Kienzie, and P. S. Yu	System for load balancing by replicating portion of file while being read by first stream onto second device and reading portion
5,802,302	J. G. Wacławsky and P. C. Hershey	System and method for response time measurement in a high speed data transmission network
5,802,306	G. T. Hunt	Protocol stack sharing multiple logical adapters
5,802,310	B. Rajaraman	Systems and method for data channel queue control in a communications network
5,802,313	G. M. Weichinger, H. Shin, J. O. Mitchell, and S. M. Shah	Extended DLUR/APPN support for non-APPN SNA devices

5,802,334	B. A. Tate and S. E. Nickolas	Method for displaying object oriented class information and content information
5,802,338	K. Ekanadham and R. N. Rechtschaffen	Method of self-parallelizing and self-parallelizing multiprocessor using the method
5,802,340	R. B. Patel and S. Mallick	Method and system of executing speculative store instructions in a parallel processing computer system
5,802,344	J. M. Menon and R. L. Mattson	Method and apparatus for dynamic segment allocation in log structured arrays
5,802,346	C. H. Olson and R. T. Golla	Method and system for minimizing the delay in executing branch-on-register instructions
5,802,350	J. D. Purcell, K. F. Reick, and K. W. Franz	System and method for selecting an interrupt system based upon the operating system of a multiprocessor system
5,802,352	J. M. Chow, L. S. Rich, M. Nally, and S. F. Griffin	Method for separating data retrieval and presentation in an object-oriented reporting system
5,802,354	D. E. Whitney, I. G. Siegel, J. P. Kubala, and T. B. Mathias	Method and apparatus for synchronizing selected logical partitions of a partitioned information handling system to a test datasource
5,802,355	K. H. Pham and R. X. Arroyo	Multi-processor system using processing of different speeds
5,802,359	C. F. Webb and M. S. Farrell	Mapping processor state into a millicode addressable processor state register array
5,802,362	C. A. Schwendiman and W. M. Lipp	System and method for enabling adapters in a computer system
5,802,363	C. Dart, D. D. Williams, and S. Merkin	BIOS dynamic emulation of multiple diskettes from a single media
5,802,371	M. Meier	Method of walking-up a call stack for a client/server program that uses remote procedure call (RPS)
5,802,377	S. R. Deshpande	Method and apparatus for implementing multiple interrupt controllers in a multi-processor computer system
5,802,378	E. Silha, E. H. Welbon, F. E. Levine, and R. L. Arndt	Performance monitoring in multiprocessor system with interrupt masking
5,802,383	D. Y. Chang and S. G. Li	Method and apparatus for displaying clusters on a computer network
5,802,386	J. A. Kahle, R. McDonald, and S. Mallick	Latency-based scheduling of instructions in a superscalar processor
5,802,388	A. R. Tannenbaum, J. M. Zetts, K. J. Lee, M. J. Galliano, M. R. Desrosiers, and W. J. Tracey	System and method for correction and confirmation dialog for hand-printed character input to a data processing system
5,802,393	C. M. Herring, R. M. Begun, and W. Greer	Computer system for detecting and accessing BIOS ROM on local bus peripheral bus or expansion bus
5,802,395	B. C. Connolly, B. G. Hazelzet, and M. W. Kellogg	High density memory modules with improved data bus performance
5,802,397	J. T. Rodell, P. G. Greenstein, and R. R. Guyette	System for storage protection from unintended I/O access using I/O protection key by providing no control by I/O key entries over access by CP entity
5,802,508	L. Morgenstern	Reasoning with rules in a multiple inheritance semantic network with exceptions
5,802,524	M. L. Wang, P. L. Flowers, R. Seiffert, S. Orban, and T. S. Lee	Method and product for integrating an object-based search engine with a parametrically archived database
5,802,525	I. Rigoutsos	Two-dimensional affine-invariant hashing defined over any two-dimensional convex domain and producing uniformly-distributed hash keys
5,802,544	H. Rodriguez, J. Thomas, J. A. Strothmann, and J. L. Combs	Addressing multiple removable memory module by remapping slot addresses
5,802,546	A. B. McNeill, D. R. Chisholm, E. I. Wachtel, G. B. Hoch, and T. V. Lee	Status handling for transfer of data blocks between a local side and a host side
5,802,547	V. J. Legvold	Data storage system with streamlined data exchange path to cached data
5,802,549	H. K. Grama, J. D. Heath, and P. Goyal	Method and apparatus for patching pages of ROM
5,802,556	R. B. Patel and S. Mallick	Method and apparatus for correcting misaligned instruction data
5,802,562	J. A. Kahle	Information processing system and including a supplemental memory and method of operation
5,802,564	A. Saha, C. Ward, N. Malik, and R. Eickemeyer	Method and apparatus for increasing processor performance
5,802,567	P. P. Liu, R. P. Singh, and S. Tung	Mechanism for managing offset and aliasing conditions within a content-addressable memory-based cache memory
5,802,569	E. R. Vanderslice and T. B. Genduso	Memory/cache support for programmable cable line length support
5,802,571	B. Konigsburg, J. S. Muhich, and S. W. White	Apparatus and method for enforcing data coherency in an information handling system having multiple hierarchical levels of cache
5,802,572	R. B. Patel and S. Mallick	Write-back cache having sub-line size coherency granularity and method for maintaining coherency within a write-back cache
5,802,573	A. Saha, N. Malik, and Z. J. Lin	Method and system for detecting the issuance and completion of processor instructions

5,802,582	B. H. Lim, K. Ekanadham, and P. Pattnaik	Explicit coherence using split-phase controls
5,802,592	D. M. Chess, G. B. Sorkin, and S. R. White	System and method for protecting integrity of alterable ROM using digital signatures
5,802,599	L. F. Cabrera and R. M. Rees	System and method for allocating storage
5,802,600	G. M. King and K. F. Smith	Method and apparatus for determining a desirable directory/data block ratio in a cache memory
5,802,700	H. A. Santini, K. Ju, M. M. Chen, and N. L. Robertson	Method of making a planarized thin film magnetic write head with submicron trackwidth
5,802,711	D. F. Card, E. Dittmann, and M. K. Saraiya	Optical module with tolerant wave soldered joints
5,804,100	B. K. Furman and M. Angelopoulos	Deaggregated electrically conductive polymers and precursors thereof
5,804,459	A. J. Gregoritsch and R. J. Bolam	Method for charge enhanced defect breakdown to improve yield and reliability
5,804,464	C. L. Bertin, K. E. Beilstein, T. H. Daubenspeck, and W. J. Howell	Semiconductor chip kerf clear method for forming semiconductor chips and electronic module therefrom
5,804,490	B. Fiegl, M. Levy, V. R. Nastasi, and W. Glashauser	Method of filling shallow trenches
5,804,503	J. Miller, J. Patrick, R. A. Previti-Kelly, and R. K. Leidy	Method and structure for reducing microelectronic short circuits using spin-on glass as part of the interlayer dielectric
5,804,607	D. Y. Shih, J. Hilborn, J. C. Hedrick, J. L. Hedrick, R. D. Miller, and Y. H. Liao	Process for making a foamed elastomeric polymer
5,804,710	B. D. Terris, D. Rugar, and H. J. Mamin	Atomic force microscope system with multi-directional voice coil actuator for controlling the stylus
5,804,803	B. J. Cragun and W. J. Rothschild	Mechanism for retrieving information using data encoded on an object
5,804,853	H. Wong, J. E. Cronin, and J. K. DeBrosse	A stacked electrical device having regions of electrical isolation and electrical connections on a given stack level
5,804,982	J. C. Lo, J. M. Hammond, and M. Servedio	Miniature probe positioning actuator
5,804,984	D. J. Alcoe and D. V. Caletka	Electronic component test apparatus with rotational probe
5,804,998	D. G. Young, J. J. Cahill, and R. R. Williams	Voltage upwardly compliant CMOS off-chip driver
5,805,086	D. F. Moertl, J. D. Brown, M. K. Edwards, and S. D. Clark	Method and system for compressing data that facilitates high-speed data decompression
5,805,088	A. S. Butter, J. P. Kuruts, L. R. Chieco, and M. A. Sorna	High speed asynchronous serial to parallel data converter
5,805,138	J. A. Strothmann, J. L. Combs, L. Sanders, and N. Brawne	Gross motion input controller for a computer system
5,805,157	D. F. Champion, L. Salcedo-Eichorn, and R. L. Bertram	Mobile client computer programmed to display hexagonal keyboard
5,805,158	D. F. Champion, P. J. Brittenham, and R. L. Bertram	Copying lists of probable widget states
5,805,159	R. L. Bertram and W. T. Oldfield	Mobile client computer interdependent display data fields
5,805,166	G. E. Hall and M. E. Molander	Segmented status area for dynamically reporting status in a data processing system
5,805,290	C. P. Ausschnitt and T. Brunner	Method of optical metrology of unresolved pattern arrays
5,805,375	H. H. Zappe, L. S. Fan, R. E. Fontana, T. Furuhashi, and T. C. Reiley	Wobble motor microactuator for fine positioning and disk drive incorporating the microactuator
5,805,382	H. P. Lee and R. G. Simmons	Low noise integrated conductor magnetic recording suspension
5,805,391	D. J. Werner, H. P. Lee, M. T. Krounbi, and T. Y. Chang	Write head with recessed stitched yoke on a planar portion of an insulation layer defining zero throat height
5,805,430	E. Kastberg, E. R. Atwood, H. Quinones, J. Benenati, J. J. Dankelman, and K. J. Puttlitz	Zero force heat sink
5,805,439	R. W. Kruppa	DC-to-DC auto switch circuit
5,805,473	S. J. Hadderman	PCMCIA voltage loss detection
5,805,475	L. E. Eisen and M. Putrino	Load-store unit and method of loading and storing single-precision floating-point registers in a double-precision architecture
5,805,487	C. H. Olson, M. Putrino, and T. A. Elliott	Method and system for fast determination of sticky and guard bits
5,805,491	R. A. Bechade	Fast 4-2 carry save adder using multiplexer logic
5,805,494	B. El Karez, R. Kleinhenz, and S. E. Schuster	Trench capacitor structures
5,805,496	K. A. Batson and R. Ross	Four device SRAM cell with single bitline
5,805,578	D. C. Verma, E. A. Hervatic, M. Gupta, P. A. Stirpe, and S. J. Nadas	Automatic reconfiguration of multipoint communication channels
5,805,589	M. M. Denneau and P. H. Hochschild	A central shared queue based time multiplexed packet switch with deadlock avoidance

5,805,594	C. M. Kotchey and R. E. Hartung	Activation sequence for a network router
5,805,606	P. G. Robertson and R. L. Tung	Cache module fault isolation technique
5,805,615	C. L. Chen	Method and apparatus for encoding certain double-error correcting and triple-error detecting codes
5,805,705	J. P. Gray, M. Peyravian, and R. O. Onvural	Synchronization of encryption/decryption keys in a data communications network
5,805,750	K. A. Bates	Optical wavefront correction for variable groove depth grating waveguide couplers
5,805,785	A. Leff, D. M. Dias, and R. P. King	Method for monitoring and recovery of subsystems in a distributed/clustered system
5,805,786	J. A. Pershing, O. T. Kirby, P. R. Badovinatz, and T. D. Chandra	Recovery of a name server managing membership of a domain of processors in a distributed computing environment
5,805,789	P. Patel, T. Lo, T. J. Slegel, and W. V. Huott	Programmable computer system element with built-in self test method and apparatus for repair during power-on
5,805,801	M. H. Holloway and T. J. Prorock	System and method for detecting and preventing security intrusions in campus LAN networks
5,805,821	A. Saxena and P. S. Wang	Video optimized media streamer user interface
5,805,822	A. J. Stagg, L. D. Long, L. F. Menditto, M. E. Miyake, and R. E. Ward	Channel interface with data segmentation and resegmentation
5,805,829	A. King, D. L. Kaminsky, G. A. Cohen, and M. MacKinnon	Process for running applets over non-IP networks
5,805,832	F. Jelinek, J. Cocke, J. C. Lai, P. F. Brown, R. L. Mercer, S. Della Pietra, and V. Della Pietra	System for parametric text to text language translation
5,805,836	A. S. Shah, K. Ward, and S. R. Barnhart	Method and apparatus for equalizing grants of a data bus to primary and secondary devices
5,805,837	R. D. Hoover and W. G. Nation	Method for optimizing reissue commands in master-slave processing systems
5,805,849	B. Konigsburg, H. Q. Le, P. J. Jordan, and S. W. White	Data processing system and method for determining an instruction order using an instruction identifier
5,805,850	D. A. Luick	Very long instruction word (VLIW) computer having efficient instruction code format
5,805,855	P. P. Liu	Interleaved data cache array having multiple content addressable fields per cache line
5,805,857	D. J. Colegrove	DASD capacity in excess of 528 megabytes apparatus and method for PC's
5,805,864	J. A. Fisher, J. W. Peake, and W. C. Carlson	Virtual integrated cartridge loader for virtual tape storage system
5,805,870	L. M. Browning	System and method for correcting clock drift in multiprocessor systems
5,805,876	H. Q. Le, K. S. Chan, P. Bose, and R. E. Wasmuth	Fast branch resolution logic circuitry
5,805,877	S. P. Song	Data processor with branch target address cache and method of operation
5,805,887	I. A. Wang	Universal pointer object
5,805,891	G. S. Bizuneh, M. J. Daniel, and R. L. Brooks	System and method for managing maintenance of computer software
5,805,897	D. Glowny	System and method for remote software configuration and distribution
5,805,898	E. L. Barsness, J. A. Larson, and R. F. Harwood	Method and apparatus for estimating installation time in a data processing system
5,805,900	B. J. North, C. E. Fulkerson, J. A. Frey, M. Kowalski, and S. A. Fagen	Method and apparatus for serializing resource access requests in a multisystem complex
5,805,906	H. Cheong, H. Q. Le, and P. J. Jordan	Method and apparatus for writing information to registers in a data processing system using a number of registers for processing instructions
5,805,907	A. J. Loper, Jr. and S. Mallick	System and method for reducing power consumption in an electronic circuit
5,805,915	J. W. Dieffenderfer, P. A. Wilkinson, and P. M. Kogge	SIMIMD array processing system
5,805,916	M. Putrino and S. Mallick	Method and apparatus for dynamic allocation of registers for intermediate floating-point results
5,805,929	B. C. Connolly, R. J. Grimm, and S. Grundon	Multiple independent I/O functions on a PCMCIA card share a single interrupt request signal using a gate for triggering a delayed reset
5,806,021	F. H. Liu, J. C. Chen, and M. A. Picheny	Automatic segmentation of continuous text using statistical approaches
5,806,056	A. Hekmatpour	Expert system and method employing hierarchical knowledge base, and interactive multimedia/hypermedia applications
5,806,080	C. Y. Yang, L. A. Williamson, and L. J. Purple	System and method for implementing canvas objects on windowing computer systems

5,806,753	D. A. Sluzewski, J. D. Bielick, M. K. Hoffmeyer, P. D. Isaacs, and T. D. Kidd	Application of low temperature metallurgical paste to form a bond structure to attach an electronic component to a carrier
5,807,165	C. Uzoh and J. Harper	Method of electrochemical mechanical planarization
5,807,439	H. Akatsu and R. Ramachandran	Apparatus and method for improved washing and drying of semiconductor wafers
5,807,455	E. J. Pega, G. Natarajan, J. G. Zhou, and M. Giordano	System and method for uniform product compressibility in a high throughput uniaxial lamination press
5,807,610	C. F. Littell, H. Cox, J. A. Trumpetto, M. S. Vanca, and R. M. Schroedl	Flex tab thick film metal mask to deposit coating material
5,807,649	D. S. O'Grady, L. Liebmann, R. A. Ferguson, and W. J. Adair	Lithographic patterning method and mask set therefor with light field trim mask
5,807,763	F. Motika, P. Nigh, and P. V. Motika	Electric field test of integrated circuit component
5,807,788	R. Conti, S. Subbanna, and S. B. Brodsky	Method for selective deposition of refractory metal and device formed thereby
5,807,791	C. L. Bertin, E. L. Hedberg, J. M. Leas, and S. H. Voldman	Methods for fabricating multichip semiconductor structures with consolidated circuitry and programmable ESD protection for input
5,808,236	H. V. Mahaney, J. R. Brezina, J. R. Taylor, and J. S. Corbin	High density heat sink attachment
5,808,268	B. Peterson, D. C. Long, J. G. Balz, and M. LaPlante	Method for marking substrates
5,808,317	Y. Kuo	Split-gate, horizontally redundant, and self-aligned thin film transistors
5,808,344	B. S. Meyerson and K. E. Ismail	Single-transistor logic and CMOS inverters
5,808,364	A. K. Stamper, J. E. Cronin, and T. Hartswick	Interconnects using metal spacers and method for forming same
5,808,474	J. Hively and M. DiPietro	Test socket for testing integrated circuit packages
5,808,494	C. G. Wright and J. Chang	Apparatus and method for generating a clock in a microprocessor
5,808,508	G. R. Castellucci and S. Tanghe	Current mirror with isolated output
5,808,601	R. C. Leah and S. J. McAllister	Interactive object selection pointer method and apparatus
5,808,605	J. M. Shieh	Virtual pointing device for touchscreens
5,808,611	J. H. Lewis, K. D. Johnson, and S. S. Stone	Positioning for multiple icons in an object-oriented interface
5,808,710	M. V. Pierson	Liquid crystal display tile interconnect structure
5,808,731	J. P. Kirk	System and method for visually determining the performance of a photolithography system
5,808,806	G. F. Guhman, M. Ray, and T. R. Touw	Optical element having regions of different refractive indices for refraction of light transmitted therethrough and wherein the regions form
5,808,832	C. Hwang, G. L. Gorman, P. C. Chen, R. Raman, and R. G. Simmons	Ultrathin silicon wear coating for thin film magnetic head
5,808,836	D. O'Regan, G. L. Heitkamp, H. L. Li, N. K. Frater, and R. Vadillo	Apparatus and method for adhesively connecting wires to a suspension arm in a disk drive assembly
5,808,853	G. J. Gaudenzi, H. M. Dalal, K. J. Travis, M. A. Takacs, and R. Y. Gorrell	Capacitor with multi-level interconnection technology
5,808,876	B. S. Mullenbach, G. A. Thompson, G. W. Westphal, and J. D. Smid	Multi-function power distribution system
5,808,891	H. S. Lee and M. E. Trumbo	A method for creating a direct hot charge rolling production schedule at a steel plant
5,808,927	J. H. Hesson	Apparatus for performing two's complement and unsigned multiply accumulate
5,808,933	B. Ross and G. A. Yeung	Zero write-cycle memory cell apparatus
5,809,232	S. Johnson and W. J. Johnson	Method and apparatus for distribution of arbitrary media
5,809,235	L. B. Turner, M. B. Sharma, and Y. T. Yeung	Object oriented network event management framework
5,809,239	A. Dan and D. Sitaram	Load balancing in servers by allocating buffer to streams with successively larger buffer requirements until the buffer requirements of a stream cannot be satisfied
5,809,268	K. S. Chan	Method and system for tracking resource allocation within a processor
5,809,291	C. M. Munozbustamante and J. W. Pearce	Interoperable 33 MHz and 66 MHz devices on the same PCI bus
5,809,292	J. W. Dieffenderfer, P. A. Wilkinson, and P. M. Kogge	Floating point for SIMD array machine
5,809,293	H. I. Linzer, J. N. Dieffenderfer, J. T. Bridges, T. A. Sartorius, T. J. Irene, and T. K. Collopy	A system and method for program execution tracing within an integrated processor
5,809,302	D. Y. Chen, F. T. Sharp, I. A. Wang, J. Ng, K. W. Hwang, and R. S. Wu	System and method for enabling pointers to be passed from computer programs written using computer programming languages that do not support pointers

5,809,323	L. E. Eisen, M. Putrino, R. B. Patel, R. T. Golla, S. Mallick, and S. Park	Method and apparatus for executing fixed-point instructions within idle execution units of a superscalar processor
5,809,390	W. G. Jackson	Device for controlling pinless paper movement in a continuous forms printer
5,809,471	M. H. Brodsky	Retrieval of additional information not found in interactive TV or telephony signal by application using dynamically extracted vocabulary
5,809,506	G. Copeland	Method for creating an object base of persistent application objects in an object oriented programming environment and apparatus related thereto
5,809,508	J. L. Howe III and K. R. Blackman	Method for capturing and cataloging datastore characteristics to define datastore persistent objects
5,809,509	J. L. Howe III and K. R. Blackman	Method for using a non-object-oriented datastore as a generic persistent datastore for persistent objects
5,809,511	J. W. Peake	Outboard data migration in a volume stacking library
5,809,525	C. E. Carmack, Jr., G. R. Klouda, J. W. Bishop, P. W. Gallagher, R. D. Siegl, and S. P. Jackowski	Multi-level computer cache system providing plural cache controllers associated with memory address ranges and having cache directories
5,809,526	R. B. Patel	Enhanced memory performance of a processor by elimination of outdated lines in a second-level cache
5,809,528	C. P. Miller and D. Pontius	Method and circuit for a least recently used replacement mechanism and invalidated address handling in a fully associative many-way cache memory
5,809,529	M. J. Mayfield	Prefetching of committed instructions from a memory to an instruction cache
5,809,537	A. Z. Qureshi, D. B. Ruth, J. C. Pescatore, and R. C. Itskin	Method and system for simultaneous processing of snoop and cache operations
5,809,546	J. T. Rodell, P. G. Greenstein, and R. R. Guyette	Method for managing I/O storage keys
5,809,548	B. R. Olszewski and J. Chang	System and method for zeroing pages with cache line invalidate instructions in an LRU system having data cache with time tags
5,809,551	G. O. Blandy	Pending page release
5,809,561	D. W. Sheffield, K. V. Nallapati, and M. D. Rogers	Method and apparatus for real memory page handling for cache optimization
5,809,566	M. Charney, P. K. Dubey, T. R. Puzak, and W. J. Starke	Automatic prefetch timing with dynamic trigger migration
5,809,641	A. H. Crudo, A. K. Sarkhel, A. K. Trivedi, C. R. Le Coz, J. G. Davis, and M. V. Pierson	Method for printed circuit board repair
5,810,241	C. F. Gabriel, J. E. Zdimal, and M. A. Jimarez	Solder bonding/debonding nozzle insert
5,810,607	B. S. Beaman, D. Y. Shih, K. E. Fogel, M. H. Norcott, and P. A. Lauro	Interconnector with contact pads having enhanced durability
5,810,924	B. S. Meyerson and F. Legoues	Low defect density/arbitrary lattice constant heteroepitaxial layers
5,810,942	W. A. Cavaliere	Aerodynamic aerosol chamber
5,811,155	M. Pinarbasi	Method of fabricating a layered magnetic head
5,811,357	D. J. Schepis, M. Armacost, M. J. Sendelbach, M. L. Passow, T. Wagner, and W. C. Wille	Process of etching an oxide layer
5,811,736	J. M. Lauffer and R. C. Senger	Electronic circuit cards with solder-filled blind vias
5,811,857	F. Assaderaghi, G. G. Shahidi, J. A. Mandelman, L. L. Hsu, and S. H. Voldman	Silicon-on-insulator body-coupled gated diode for electrostatic discharge (ESD) and analog applications
5,811,868	C. L. Bertin, J. Zalesinski, W. J. Howell, and W. R. Tonti	Integrated high-performance decoupling capacitor
5,811,870	A. Bhattacharyya, C. H. Lam, R. K. Leidy, and R. M. Geffken	Antifuse structure
5,811,962	J. Ceccherelli and T. M. Cowell	Power supply control circuit
5,811,982	B. S. Beaman, D. Y. Shih, K. E. Fogel, M. H. Norcott, and P. A. Lauro	High density cantilevered probe for electronic devices
5,811,988	G. S. Ditlow and P. Kartschoke	PLA late signal circuitry using a specialized gap cell and PLA late signal circuitry using switched output
5,811,993	R. H. Dennard and T. V. Rajeevakumar	Supply voltage independent bandgap based reference generator circuit for SOI/bulk CMOS technologies
5,812,065	A. Schrott, R. J. Gambino, and R. J. Von Gutfeld	Modulation of the resonant frequency of a circuit using an energy field
5,812,118	J. M. Shieh	Method, apparatus, and memory for creating at least two virtual pointing devices
5,812,125	J. A. Wilkinson	Method and apparatus for selectively generating display images
5,812,128	M. D. Sterling	User defined template arrangement of objects in a container

5,812,129	C. Sellers and R. W. Kacor	Method and system for accessing functions of a user interface environment from processes running outside of the user interface environment
5,812,130	G. A. Van Huben, J. L. Mueller, M. S. Siegel, and T. B. Warnock	Data management system and method for concurrent engineering
5,812,131	R. L. Bertram	Mobile client computer programmed to process table displays
5,812,135	C. M. Kotchey	Reorganization of nodes in a partial view of hierarchical information
5,812,137	G. H. Ottaway, M. T. Peets, and R. P. Kerker	Compliant sectioning facility for interactive sectioning of solid geometric objects using a graphics processor
5,812,144	B. Potu and K. L. Hill	System for performing real-time video resizing in a data processing system having multimedia capability
5,812,341	R. W. Luoma and Z. E. Boutaghou	Laminated back iron structure for increased motor efficiency
5,812,372	G. T. Galyon, R. G. Kemink, and R. R. Schmidt	Tube in plate heat sink
5,812,418	G. M. Lattimore, J. S. Muhich, and R. P. Masleid	Cache sub-array method and apparatus for use in microprocessor integrated circuits
5,812,521	N. V. Phan and S. B. Levenstein	Static adder using BICMOS emitter dot circuits
5,812,535	B. D. Mandalia, C. Hallock, C. Thomson, C. H. Sederholm, G. J. Salem, H. Parikh, and W. J. Shomar	Methods for performing intelligent network services with an ISDN network terminator located at a subscriber's premise
5,812,549	H. Sethu	Route restrictions for deadlock free routing with increased bandwidth in a multi-stage cross point packet switch
5,812,767	G. Joyce, J. A. Haug, L. S. Wise, L. W. Russell, and S. G. Desai	Method and apparatus for improving performance of a data link provider interface in a network environment
5,812,804	C. Bates, E. E. Burris, and J. M. Ryan	Display apparatus for tossing windows
5,812,811	C. Chuang, C. M. Barton, K. O'Brien, L. H. Lam, and P. K. Dubey	Executing speculative parallel instruction threads with forking and inter-thread communication
5,812,812	M. Sharma, R. McDonald, and S. Mallick	Method and system of implementing an early data dependency resolution mechanism in a high-performance data processing system utilizing out-of-order instruction issue
5,812,817	D. A. Luick, J. D. Brown, K. H. Haselhorst, S. W. Kerchberger, and W. P. Hovis	Compression architecture for system memory applications
5,812,823	A. M. Denicolas, J. A. Kahle, and S. Mallick	Method and system for performing an emulation context save and restore that is transparent to the operating system
5,812,838	J. A. Silberman, P. S. Lin, and S. H. Dhong	Branch history table
5,812,847	R. V. Joshi and S. R. Joshi	Rule-based method for designing user interfaces for applications
5,812,854	P. J. Steinmetz	Mechanism for integrating user-defined instructions with compiler-generated instructions and for optimizing the integrated instruction stream
5,812,864	E. Yiskis and G. C. McCoy	Personal computer/host emulation system for handling host data with personal computer application programs at personal computers
5,812,881	T. Q. Nguyen and Y. M. Ku	Handshake minimizing serial to parallel bus interface in a data processing system
5,812,930	R. J. Zavrel	Information handling system with broadband and narrowband communication channels between repository and display systems
5,812,989	C. Wang, D. E. Witt, and J. J. Potter	Image statement preparation by work flow management using statement cycles and statement interactive sessions
5,813,002	A. Arning and R. Agrawal	Method and system for linearly detecting data deviations in a large database
5,813,003	M. S. Chen and P. S. Yu	Progressive method and system for CPU and I/O cost reduction for mining association rules
5,813,004	D. L. Meck and W. J. Roden	Partitioning and sorting logical units of data prior to reaching an end of the data file
5,813,015	R. A. Pascoe	Method and apparatus for increasing available storage space on a computer system by disposing of data with user defined characteristics
5,813,017	R. J. Morris	System and method for reducing storage requirement in backup subsystems utilizing segmented compression and differencing
5,813,020	J. L. Sampson, J. R. Sampson, J. W. Marlin, and R. H. Hohensee	Method and system for dynamic presentation parameter override during document interchange
5,813,032	G. Bhargava, I. S. Narang, and J. Z. Teng	System and method for continuous destaging of changed data in a multisystem shared disk environment
5,813,042	B. Rajaraman, L. D. Long, L. H. Overby, and S. A. Campbell	Methods and systems for control of memory
5,813,082	L. C. Stevens	Pad and roller assembly for cleaning semiconductor wafers
5,813,113	G. L. Stewart and M. Dunaj	Fixture for making laminated integrated circuit devices
5,813,781	J. D. Thomas, K. H. Vorhees, and R. H. Harris	Document feed roller opener and method therefor

5,813,870	G. Saxenmeyer, G. F. Walker, J. A. Molla, J. M. Roldan, M. Gaynes, and S. P. Ostrander	Selectively filled adhesives for semiconductor chip interconnection and encapsulation
5,814,846	A. Essbaum and B. A. Zoric	Cell apparatus and method for use in building complex integrated circuit devices
5,814,877	H. Shaukatullah and S. J. Diffenderfer	Single layer leadframe design with groundplane capability
5,814,885	H. B. Pogge, H. L. Kalter, and J. Greschner	Very dense integrated circuit package
5,814,980	S. C. Lewis	Wide range voltage regulator
5,815,009	J. A. Iadanza and M. Ueda	Process tolerant delay circuit having process sensitive and process insensitive components intentionally placed therein
5,815,106	J. M. Poss and T. J. Schmerbeck	Split flash analog to digital converter differential driver
5,815,107	M. M. Smadi and R. F. Frankeny	Current source referenced high speed dialog to digital converter
5,815,120	M. J. Lawrence and W. B. Nunnery	Radio frequency local area network adapter card structure and method of manufacture
5,815,142	B. K. Tiller, C. Au, C. S. Lanier, D. Allard, D. G. Johnson, D. M. Hsieh, F. J. Canova, J. F. Goodwin, J. R. Lewis, R. L. Yee, and W. Villafana	Apparatus and method for marking text on a display screen in a personal communications device
5,815,144	J. A. Strothmann	Icon based reset for cartridge memory computer system
5,815,153	C. A. Swearingen and S. H. Isensee	Multifunction control with changeable appearance
5,815,157	A. D. Bowen	Method and apparatus for texture border address mapping in rectangular coordinates
5,815,259	D. Gilbert, J. R. Lankard, Jr., and M. Brandon	Quick change housing
5,815,347	S. Pattanaik	Information storage system having an improved planar head-suspension assembly
5,815,354	D. A. Evans, G. M. Bracer, and J. Connor	Receiver input voltage protection circuit
5,815,374	W. J. Howell	Method and apparatus for redirecting certain input/output connections of integrated circuit chip configurations
5,815,377	D. G. Lund, J. L. Peacock, and S. A. Shurson	Apparatus for auto docking PCI cards
5,815,406	C. H. Olson and R. T. Golla	Method and system for designing a circuit using RC and timing weighting of nets
5,815,455	B. T. Hoang, C. Nguyen, G. Tsao, G. D. Carpenter, H. C. Tanner, and K. A. Braithwaite	Power supply interface circuit providing non-volatile storage with suitable operating and standby voltage levels
5,815,516	A. A. Slane, G. S. Delp, K. D. Aaker, and L. A. Sendelbach	Method and apparatus for producing transmission control protocol checksums using internet protocol fragmentation
5,815,573	C. W. Kaufman, D. B. Johnson, M. M. Yung, N. Zunic, P. A. Karger, and S. M. Matyas	Cryptographic key recovery system
5,815,647	D. Neal, P. A. Buckland, and S. M. Thurber	Error recovery by isolation of peripheral components in a data processing system
5,815,650	A. B. Johnson, J. T. Brady, N. Apperley, and S. D. Gerdt	Efficient system for predicting and processing storage subsystem failure
5,815,656	C. S. Michod, D. A. Kukula, G. E. Martin, J. N. McCauley, K. W. Lane, M. A. Reid, M. H. Hartung, S. K. Candelaria, V. J. Legvold, and W. L. Richardson	Cache reinitialization conflict (CRIC)
5,815,687	A. M. Youngs, R. P. Masleid, and W. Roesner	Apparatus and method for simulating domino logic circuits using a special machine cycle to validate pre-charge
5,815,694	C. L. Johnson, J. D. Strom, and P. A. Ganfield	Apparatus and method to change a processor clock frequency
5,815,716	T. Horiguchi	Method for supporting multiple call interface conventions for computer programs using compiler directives
5,815,723	J. W. Dieffenderfer, P. A. Wilkinson, P. M. Kogge, and T. N. Barker	Picket autonomy on a SIMD machine
5,815,731	R. P. Doyle and S. J. Schlosser	Method and system for providing device driver configurations on demand
5,815,942	J. J. Wu, R. Arndt, S. Nadahara, and S. L. Cohen	Vapor drying system and method
5,817,405	A. Bhatt, A. C. Bhatt, J. A. Knight, R. J. Day, R. W. Malek, T. P. Duffy, and V. R. Markovich	Method of making a circuitized substrate using two different metallization processes
5,817,543	G. J. Robbins, G. M. Wilkins, J. A. McDonald, M. McAllister, and M. Swaminathan	Method of constructing an integrated circuit memory
5,817,986	A. Viehbeck, D. Lewis, E. Davidson, J. M. Shaw, and J. S. Wilczynski	Three dimensional package and architecture for high performance computer

5,818,099	J. Burghartz	MOS high frequency switch circuit using a variable well bias
5,818,107	M. V. Pierson and T. B. Youngs, Jr.	Chip stacking by edge metallization
5,818,110	J. E. Cronin	Integrated circuit chip wiring structure with crossover capability and method of manufacturing the same
5,818,239	M. E. Scaman	Simplified contactless test of MCM thin film I/O nets using a plasma
5,818,264	D. G. Mikan and M. K. Ciraula	Dynamic circuit having improved noise immunity and method therefor
5,818,280	D. E. Martin	Method and apparatus with preconditioning for shifting the voltage level of a signal
5,818,444	C. C. Alimpich, C. L. Doescher, G. D. Boldt, J. S. Goddard, L. L. Vigil, and M. T. Vo	Method, apparatus and application for object selective but global attribute modification
5,818,446	M. J. Brozowski, P. J. Brittenham, and R. L. Bertram	System for changing user interfaces based on display data content
5,818,451	D. F. Champion, M. T. Hartman, and R. L. Bertram	Computer programmed soft keyboard system, method and apparatus having user input displacement
5,818,469	A. E. Putney, B. Poddar, H. Smit, and J. J. Lawless	Graphics interface processing methodology in symmetric multiprocessing on distributed network environments
5,818,563	C. Narayan, E. Colgan, J. L. Sanford, K. H. Yang, R. L. Melcher, and T. Nakasoki	Substrate structure assembly method for reduced spatial light modulator size
5,818,656	K. B. Klaassen and R. J. Reay	Circuitry for correction of thermal transients in the analog signal from a magnetoresistive sensor
5,818,657	K. Serizawa, M. Ohta, M. Sri-Jayantha, S. Kumar, and V. D. Khanna	Disk drive with impulse torque generating element
5,818,659	C. C. Liu, F. E. Mueller, and W. L. Cheung	Quadrature servo pattern disk providing asynchronous digital PES
5,818,661	R. W. Luoma and Z. E. Boutaghoul	Laminated back iron structure for increased motor efficiency
5,818,662	W. C. Shum	Static attitude and stiffness control for an integrated suspension
5,818,697	G. J. Armezzani, M. V. Pierson, R. N. Ives, and T. A. Tull	Flexible thin film ball grid array containing solder mask
5,818,715	B. Marshall and T. Raz	Method and system for modifying a project model
5,818,723	K. Dimitri	Quick access data storage library with backup capability
5,818,748	C. L. Bertin and J. E. Cronin	Chip function separation onto separate stacked chips
5,818,819	B. D. Mandalia, C. Hallock, C. Thomson, C. H. Sederholm, G. J. Salem, H. Parikh, and W. J. Shomar	Methods for performing intelligent network services with an ISDN network terminator located at a subscriber's premise
5,818,826	B. Rezvani, C. M. Olsen, D. N. McKay, F. R. Gfeller, M. Naghshineh, P. Kam, P. Kermani, and P. D. Hortensius	Media access control protocols in a wireless communication network supporting multiple transmission rates
5,818,984	E. R. Atwood and U. M. Ahmad	Optoelectronic interconnection of integrated circuits
5,819,011	G. Fitzpatrick, M. L. Williams, and W. J. Johnson	Media boundary traversal method and apparatus
5,819,043	A. P. Stephens and M. J. Baugher	Multimedia resource reservation system
5,819,061	A. H. Preston, D. E. Stucki, F. Cox, M. Kim, M. A. Krygowski, N. T. Christensen, R. F. Hill, R. S. Capowski, S. G. Glassen, and T. O. Curlee	Method and apparatus for dynamic storage reconfiguration in a partitioned environment
5,819,063	J. C. Endicott, J. H. Peterson, P. J. Heyrman, R. G. Mustain, R. K. Kirkman, and S. A. Dahl	Method and data processing system for emulating a program
5,819,078	B. W. Krumm, C. F. Webb, J. A. Navarro, and M. S. Farrell	Addressing extended memory using millicode
5,819,083	S. G. Li and W. T. Chen	Minimal sufficient buffer space for data redistribution in a parallel database system
5,819,091	J. M. Phelan, J. W. Arendt, and R. K. Manikundalam	User level control of degree of client side processing
5,819,095	A. X. Tra, L. B. Capps, Jr., and S. H. Lam	Method and apparatus for upgrading a PCI system to a CHRP system
5,819,225	A. G. Klein, A. J. Happ, M. Milenkovic, and P. R. Eastwood	Visual feedback during speech interaction
5,819,249	L. A. Dohanich and S. L. Dohanich	Tax coding expert system
5,819,265	M. A. Choi, N. Wacholder, and Y. Ravin	Processing names in a text
5,819,266	R. Agrawal and R. Srikant	System and method for mining sequential patterns in a large database
5,819,268	T. P. Hackett	Method and system for testing for equality/difference in multiple tables of a database
5,819,276	A. Hassitt, K. M. Ketell, M. Z. Smith, T. Horiguchi, and Y. J. Cai	Method for supporting multiple file systems in file input/output operations

5,819,402	A. Fahey, C. F. Jones, D. L. Edwards, G. Messina, G. S. Hopper, M. G. Courtney, R. A. Sherif, and S. Iruvanti	Method for cooling of chips using blind holes with customized depth
5,819,652	A. S. Shah, J. Utter, and M. J. Matts	Reduced cavity depth screening stencil
5,820,697	C. J. Merz III, J. D. Poole, M. R. Hamilton, and M. V. Malone	Fluorescent water soluble solder flux
5,821,161	D. L. Edwards, H. T. Toy, J. Covell, L. R. Bolde, L. S. Goldmann, and P. A. Gruber	Cast metal seal for semiconductor substrates and process thereof
5,821,542	S. D. Golladay	Particle beam imaging system having hollow beam illumination
5,821,577	B. S. Meyerson, E. F. Crabbe, J. M. Stork, and S. Vandebroek	Graded channel field effect transistor
5,821,592	H. Hoenigschmid and J. DeBrosse	Dynamic random access memory arrays and methods therefor
5,821,759	A. J. Castellano, E. J. Yarmchuk, M. E. Scaman, and Y. Y. Yu	Method and apparatus for detecting shorts in a multi-layer electronic package
5,821,763	B. S. Beaman, D. Y. Shih, G. F. Walker, K. E. Fogel, M. H. Norcott, and P. A. Lauro	Test probe for high density integrated circuits methods of fabrication thereof and methods of use thereof
5,821,809	D. M. Dreps and D. W. Boerstler	CMOS high-speed differential to single-ended converter circuit
5,821,810	D. P. Swart and G. S. Winn	Method and apparatus for trim adjustment of a variable gain amplifier
5,821,850	J. Beck	Method and apparatus for comparing magnitude of data from a plurality of data sources
5,821,927	Q. Gong	Web browser display indicator signalling that currently displayed web page needs to be refreshed from remote source
5,821,928	D. J. Schell and L. A. Melkus	Method and system for controlling individual messages
5,821,933	C. A. Pickover and N. Keller	Visual access to restricted functions represented on a graphical user interface
5,822,042	C. A. Rudisill, D. J. Whittle, and D. S. Goodman	Three-dimensional imaging system
5,822,141	C. S. Nomura, J. T. Contreras, and P. W. Chung	High speed FET write driver for an inductive head
5,822,211	A. Tam, A. R. Taheri, B. Karni, B. M. Harper, E. V. Schnetzer, H. Lu, H. Seing, L. T. Pena, M. Barenboim, M. G. Lisanke, P. Baumgart, P. J. Kerstens, and P. P. Chrusch, Jr.	Laser texturing apparatus with dual laser paths having an independently adjusted parameter
5,822,268	T. Kirihiata	Hierarchical column select line architecture for multi-bank DRAMs
5,822,285	B. D. Terris and D. Rugar	Atomic force microscopy data storage system with tracking servo
5,822,328	A. Satt, J. H. Derby, and U. Shvadron	Frame synchronization mechanism for digital simultaneous voice/data modems
5,822,330	P. A. Buckland	A method and system for dynamically adjusting signal skewing
5,822,338	T. Lo	ECC-compare path of cache directory logic improvements
5,822,426	J. W. Sylvant and T. M. Rasmus	A balanced hybrid circuit
5,822,506	D. E. Stone, F. S. Shook, J. M. Nagda, J. W. Marlin, R. H. Hohensee, and W. Chen	Selectable modification of medium in a printer system
5,822,531	T. V. Weaver	Method and system for dynamically reconfiguring a cluster of computer systems
5,822,543	B. E. Willner, E. H. Stern, and J. M. Dunn	Gathering data handling statistics in non-synchronous data communication networks
5,822,556	C. H. Olson, M. T. Vaden, and T. M. Potter	Distributed completion control in a microprocessor
5,822,562	A. A. Helffrich, D. A. Elko, D. J. Dahlen, J. M. Nick, R. King, S. Palmer, and W. W. Wilkinson	Method and apparatus for expansion, contraction, and reapportionment of structured external storage structures
5,822,576	J. R. Robinson and R. Dinkjian	Branch history table with branch pattern field
5,822,577	K. Ekanadham and R. N. Rechtschaffen	Context oriented branch history table
5,822,596	H. F. Casal, H. H. Li, N. G. Thoma, and T. Nguyen	Controlling power up using clock gating
5,822,608	J. W. Dieffenderfer, N. J. Schoonover, P. A. Wilkinson, and P. M. Kogge	Associative parallel processing system
5,822,620	A. Pita, A. Saha, N. Malik, and S. R. Vohra	Method and apparatus for data alignment
5,822,714	R. T. Cato	Data processing system and method for accessing a plurality of radio frequency identification tags
5,822,718	A. Nassiff, F. Fado, P. Guasti, and R. Bakis	Device and method for performing diagnostics on a microphone
5,822,719	J. M. Kurtzberg and M. Levanoni	Performance-constrained component selection

5,822,745	A. Hekmatpour	Expert system and method employing hierarchical knowledge base and interactive multimedia/hypermedia applications
5,822,750	M. Jou, M. H. Pirahesh, and T. Y. Leung	Optimization in correlated SQL queries in a relational database management system
5,822,752	H. Cheong, H. Q. Le, J. S. Muhich, and M. K. Ciraula	Method and apparatus for fast parallel determination of queue entries
5,822,755	D. J. Shippy	Microprocessor architecture having dual usage cache tag array
5,822,758	A. J. Loper, Jr., C. H. Olson, D. J. Shippy, and T. A. Elliott	Method and system for high performance cache arbitration
5,822,763	S. J. Baylor and Y. Hsu	Cache coherence protocol for reducing the effects of false sharing in non-bus-based shared-memory multiprocessors
5,822,765	B. D. Boatright, D. E. Williams, K. A. Feiste, and L. J. Merkel	System and method for resolving execution of an operation in a computing system with distributed memory
5,822,856	A. C. Bhatt, A. F. Moring, D. H. Glatzel, D. J. Russell, K. I. Papathomas, and V. R. Markovich	Manufacturing circuit board assemblies having filled vias
5,823,696	R. G. King and R. H. Harris	Printer having a paper out/cover sensor and method therefor
5,824,157	A. Viehbeck, E. Foster, J. C. Hedrick, K. Papathomas, R. M. Japp, and S. L. Tisdale	Fluid jet impregnation
5,824,568	J. H. Zechman	A process of making an integrated circuit chip composite
5,824,580	M. Hauf, M. Levy, and V. R. Nastasi	Method of manufacturing an insulated gate field effect transistor
5,825,039	J. G. Hartley	Digitally stepped deflection raster system and method of use thereof
5,825,087	G. Messina, M. E. Klepeis, R. A. Sherif, and S. Iruvanti	Integral mesh flat plate cooling module
5,825,169	K. D. Selander and M. A. Sorna	Dynamically biased current gain voltage regulator with low quiescent power consumption
5,825,208	H. L. Levy and S. A. Shah	Method and apparatus for fast evaluation of dynamic CMOS logic circuits
5,825,226	F. D. Ferraiolo, I. I. Novof, and J. E. Gersbach	Delay equalization apparatus and method
5,825,245	M. S. Michail and W. D. Pricer	Compound cascode amplifier
5,825,369	G. Taubin and J. Rossignac	Compression of simple geometric models using spanning trees
5,825,506	C. Curley, D. Mazina, F. J. Yeskel, G. M. Bednar, L. P. Curley, P. L. Olson, and T. E. Carr	Method and apparatus for image processing at remote sites
5,825,579	D. L. Clare, L. J. Serrano, and W. L. Cheung	Disk drive servo sensing gain normalization and linearization
5,825,587	L. S. Samuelson and S. A. Bolasna	Shallow etch air bearing surface features for optimized transducer spacing
5,825,588	S. Gopalakrishna and S. A. Bolasna	Roll insensitive air bearing slider
5,825,595	H. S. Gill	Spin valve sensor with differential detection
5,825,609	D. E. Kotecki, K. Saenger, and P. C. Andricacos	Compound electrode stack capacitor
5,825,620	G. M. Chrysler and R. C. Chu	Enhanced air cooling system with attached cooling unit
5,825,629	J. P. Partridge and K. G. Hoebener	Print circuit board product with stencil controlled fine pitch solder formation for fine and coarse pitch component attachment
5,825,661	A. D. Drumm	Method and apparatus for automatic post-layout optimization of an integrated circuit
5,825,677	B. Olsson, F. G. Gustavson, M. A. Johnson, R. C. Agarwal, and R. D. Groves	Numerically intensive computer accelerator
5,825,678	R. M. Smith	Method and apparatus for determining floating point class
5,825,744	A. R. Clark, G. A. Jaquette, and R. A. Hutchins	Configurable read detection channel and method for decoding data stored within a data storage medium
5,825,748	C. Bender, D. G. Smyth, D. L. Garmire, H. E. Roman, and K. S. Barkey	Credit based flow control checking and correction system
5,825,785	J. D. Chickanosky, M. R. Ouellette, R. L. Barry, and S. F. Oakland	Serial input shift register built-in self test circuit for embedded circuits
5,825,877	A. Dan, D. Sitaram, and R. Ramaswami	Support for portable trusted software
5,825,892	F. C. Mintzer and G. W. Braudaway	Protecting images with an image watermark
5,825,949	F. Janniello, F. F. Tong, J. K. Kravitz, K. Liu, M. M. Choy, P. E. Green, R. Ramaswami, and W. E. Hall	Optical wavelength division multiplexer for coupling to data sources and sinks wherein at least two data sources and sinks operate with different communication protocols
5,826,006	T. Saitoh	Method and apparatus for testing the data output system of a memory system
5,826,023	G. J. Romano and M. S. Hall	Communications tunneling
5,826,029	J. F. Haugh and R. C. Gore, Jr.	Secured gateway interface

5,826,046	C. H. Tran, T. B. Bui, and T. T. Nguyen	Method and apparatus for polling and selecting any paired device in any drawer
5,826,062	J. W. Fake, Jr. and S. S. Gruber	Method and apparatus for converting and displaying a multimedia document at a client
5,826,064	K. Loring and W. Shaouy	User-configurable earcon event engine
5,826,065	F. A. Houle and W. D. Hinsberg	Software architecture for stochastic simulation of nonhomogeneous systems
5,826,070	C. H. Olson, J. S. Brooks, and M. S. Schmookler	Apparatus and method for maintaining status flags and condition codes using a renaming technique in an out of order floating point execution unit
5,826,075	R. Bealkowski and R. M. Begun	Automated programmable firmware store for a personal computer system
5,826,090	B. G. Mealey, M. S. Williams, and R. C. Swanberg	Loadable hardware support
5,826,215	A. E. Ruehli and J. E. Garrett	Method and computer program product for stable integral equation models
5,826,260	J. M. Prager, M. N. Wegman, R. J. Byrd, and Y. Ravin	Information retrieval system and method for displaying and ordering information based on query element contribution
5,826,262	B. R. Iyer, G. R. Ricard, S. D. Helt, and T. Bui	Parallel bottom-up construction of radix trees
5,826,265	G. A. Van Huben, J. C. Mak, J. L. Mueller, and S. Y. Xiao	Data management system having shared libraries
5,826,325	B. J. Kriehn, B. V. Johnson, E. A. Eckberg, K. B. Price, and R. T. Flowers	Hard disk file design for improved head merge process in tub casting
5,826,328	F. M. Guindon, M. J. Brady, P. Murphy, P. A. Chalco, and P. A. Moskowitz	Method of making thin radio frequency transponder
5,826,643	G. T. Galyon, R. G. Kemink, and R. R. Schmidt	A method of cooling electronic devices using a tube in plate heat sink
5,827,386	A. Bhatt, K. J. Lubert, and M. D. Derwin	Method of forming a multi-layered circuitized substrate member
5,827,414	J. D. Westwood	Single piece slotted ferromagnetic sputtering target and sputtering apparatus
5,827,424	A. Homola, D. H. Jen, D. R. Gillis, and E. T. Schreck	Contaminant reduction system for disk drives
5,827,622	H. Coufal and R. K. Grygier	Reflective lithographic mask
5,827,765	A. P. Short, E. N. Hammerl, H. Ho, J. A. Mandelman, R. Srinivasan, and R. Stengl	Buried-strap formation in a DRAM trench capacitor
5,827,907	A. Viehbeck, J. C. Hedrick, J. T. Gotro, K. Papathomas, N. M. Patel, and W. D. Joseph	Homo-, co- or multicomponent thermoplastic polymer dispersed in a thermoset resin
5,828,031	S. Pattanaik	Head transducer to suspension lead termination by solder ball place/reflow
5,828,131	C. Cabral, F. M. d'Heurle, G. L. Miles, J. M. Harper, J. S. Nakos, K. L. Saenger, L. Clevenger, R. A. Roy, and R. W. Mann	Low temperature formation of low resistivity titanium silicide
5,828,239	Y. Lotfi	Sense amplified circuit with minimized clock skew effect
5,828,255	I. I. Novof, R. Kelkar, and S. Wyatt	Phase locked loop having adaptive jitter reduction
5,828,257	R. P. Masleid	Precision time interval division with digital phase delay lines
5,828,259	L. Wu and M. Cases	Method and apparatus for reducing disturbances on an integrated circuit
5,828,318	C. Cesar	System and method for selecting a subset of autonomous and independent slave entities
5,828,349	B. J. Machesney and R. M. West	Method and system for multiplexing and demultiplexing video signals for graphic display monitors in computer systems
5,828,371	J. H. Werner, R. L. Poston, S. H. Isensee, and T. L. Cline	Method and system for graphical video image presentation control
5,828,481	C. J. Freedenberg, F. Topolovec, J. M. Cobb, and U. A. Ziemins	Mid-objective laser scanner
5,828,513	R. Greenberg	Servo address apparatus and positioning methods for read, write and seek operations in a direct access storage device
5,828,514	A. Chliwnyj and S. C. Wills	Servo error detection in a noisy environment
5,828,522	D. H. Brown and Z. E. Boutaghou	Velocity control of head load/unload mechanism in a disk drive using dither
5,828,529	H. S. Gill	Antiparallel pinned spin valve with read signal symmetry
5,828,530	B. A. Gurney, D. J. Werner, H. S. Gill, J. F. Smyth, and V. S. Speriosu	Orthogonal spin valve sensor with reduced net field
5,828,531	H. S. Gill	Spin valve sensor with enhanced magnetoresistance

5,828,532	J. K. Howard, M. A. Parker, and R. H. Ahlert	Getter layer lead structure for eliminating resistance increase phenomena and embrittlement and method for making the same
5,828,836	G. E. Westwick and M. B. Bhuta	Networked information communication system
5,828,894	J. W. Dieffenderfer, P. A. Wilkinson, and P. M. Kogge	Array processor having grouping of SIMD pickets
5,828,895	K. S. Chan and R. Nair	Method and system for predecoding instructions in a superscalar data processing system
5,829,006	B. Parvathaneny and V. Srinivasan	System and method for efficient relational query generation and tuple-to-object translation in an object-relational gateway supporting class inheritance
5,829,018	D. E. Becker, D. F. Moertl, G. S. Van Grinsven, L. D. Cleveland, and M. D. Christianson	Apparatus and method for writing data from a cache to a storage device
5,829,124	D. N. Light and J. S. Kresge	Method for forming metallized patterns on the top surface of a printed circuit board
5,830,332	A. C. Callegari, E. D. Babich, F. E. Doany, and S. Purushothaman	Sputter deposition of hydrogenated amorphous carbon film and applications thereof
5,830,374	C. M. Boyko, I. S. Mahmoud, J. M. Lauffer, and R. C. McHatton	Method for producing multi-layer circuit board and resulting article of manufacture
5,830,514	A. Tam, B. Karni, H. Seing, M. Barenboim, P. Baumgart, P. J. Kerstens, and P. P. Chrusch, Jr.	Controlling pulses in a laser texturing tool
5,830,538	J. R. Heath and S. M. Gates	Method to form a polycrystalline film on a substrate
5,831,153	G. Binnig, G. K. Binnig, and W. Haeblerle	Investigation and/or manipulation device for a sample in fluid
5,831,283	J. Batey, J. H. Souk, and P. M. Fryer	Passivation of copper with ammonia-free silicon nitride and application to TFT/LCD
5,831,301	D. V. Horak, J. A. Mandelman, M. C. Hakey, S. J. Holmes, T. Furukawa, and W. H. Ma	Trench storage DRAM cell including a step transfer device
5,831,336	G. DiGiacomo	Ternary solder for the enhancement of C-4 fatigue life
5,831,452	E. J. Nowak, L. G. Heller, and M. H. Tong	Leak tolerant low power dynamic circuits
5,831,459	T. C. McDonald	Method and system for adjusting a clock signal within electronic circuitry
5,831,464	K. W. Lang	Simplified differential single-shot
5,831,484	E. J. Lukes and J. D. Strom	Differential charge pump for phase locked loop circuits
5,831,532	A. Schrott, C. Jahnes, M. Paunovic, R. J. Gambino, and R. J. Von Gutfield	Identification tags using amorphous wire
5,831,593	J. D. Rutledge	Low-power standby mode for a remote sensing device
5,831,607	A. P. Brooks	Technique to adapt multiple screens' information onto a single GUI panel
5,831,638	E. K. Evans and R. M. West	Graphics display system and method for providing internally timed time-varying properties of display attributes
5,831,698	A. C. Lowe, R. L. Wisnieff, and S. W. Depp	Electrically variable diffuser
5,831,710	E. Colgan, J. L. Sanford, K. H. Yang, M. Lu, and R. L. Melcher	Liquid crystal display
5,831,786	E. A. Cunningham, H. H. Ottesen, and Z. E. Boutaghoul	Disk drive having position sensor for ramp load and unload, and method for its use
5,831,810	F. F. Cappel, Jr., J. L. Frankel, K. A. Bird, P. J. Brofman, S. Knickerbocker, S. A. Sikorski, and S. D. Kadakia	Electronic component package with decoupling capacitors completely within die receiving cavity of substrate
5,831,828	C. S. Milkovich, E. A. Johnson, J. M. Zalesinski, J. S. Perkins, L. R. Cutting, M. A. Gaynes, M. V. Pierson, and S. E. Poetzinger	Flexible circuit board and common heat spreader assembly
5,831,870	A. C. Folta, P. G. Villarrubia, P. T. Patel, and S. Mehrotra	Method and system for characterizing interconnect data within an integrated circuit for facilitating parasitic capacitance estimation
5,831,896	G. A. Yeung, G. M. Lattimore, and T. L. Leasure	Memory cell
5,831,912	G. Mueller and T. Kirihata	Semiconductor memory having space-efficient layout
5,831,913	T. Kirihata	Method of making a memory fault-tolerant using a variable size redundancy replacement configuration
5,831,914	T. Kirihata	Variable size redundancy replacement architecture to make a memory fault-tolerant
5,832,005	S. Singh	Fault-tolerant method and means for managing access to an initial program load stored in read-only memory or the like
5,832,047	D. Casper, F. D. Ferraiolo, R. C. Jordan, R. S. Capowski, and W. Laviola	Self-timed interface

5,832,048	G. R. Woodman	Digital phase-locked loop control system
5,832,127	P. D. Healey	Apparatus and fast method for convex-polygon bounding of closed contours
5,832,199	A. B. Johnson, J. T. Brady, N. Apperley, and S. D. Gerdt	Efficient system for predicting and processing storage subsystem failure
5,832,211	G. R. Blakley III, I. M. Milman, and W. Sigler	Propagating plain-text passwords from a main registry to a plurality of foreign registries
5,832,212	B. J. Cragun and P. R. Day	Censoring browser for internet viewing
5,832,218	R. T. Gibbs and S. Jaji	Client/server electronic mail system for providing off-line client utilization and seamless server resynchronization
5,832,220	R. P. Welch and W. J. Johnson	Automatic setting of an acknowledgement option based upon distribution content in a data processing system
5,832,239	D. Gillen, D. D. Gavin, J. Partridge, J. A. Haug, and L. W. Russell	High performance communications path
5,832,268	A. Tin, C. B. Harvey, F. L. Fitzpatrick, J. B. Anderson, and J. R. Wason	System and method for supporting complex objects in an object oriented computing environment
5,832,270	A. Malhotra and C. Laffra	System having automatic insertion of hooks into object-oriented software for visualizing execution thereof
5,832,276	K. A. Feiste and T. J. Somyak	Resolving processor and system bus address collisions in a high-level cache
5,832,280	R. C. Swanberg	Method and system in a data processing system for interfacing an operating system with a power management controller
5,832,284	M. S. Michail, S. T. Ventrone, and W. D. Pricer	Self regulating temperature/performance/voltage scheme for micros (X86)
5,832,309	B. D. Noe, B. W. Lawton, D. W. Killian, and M. J. Koval	Method and apparatus for synchronizing streaming and non-streaming multimedia devices
5,832,441	C. K. Laws, C. V. Pinera, J. D. Aaron, P. T. Brunet, and R. Mahaffey	Creating speech models
5,832,460	G. M. Bednar, J. W. Donahue, R. F. Hendrix, R. J. Kuklantz, T. E. Carr, and W. A. Uhrich	Method and system for bill presentation and payment reconciliation
5,832,475	A. Gupta, R. Agrawal, and S. Sarawagi	Database system and method employing data cube operator for group-by operations
5,832,477	B. Iyer, G. Bhargava, and P. Goel	Method and apparatus for reordering complex SQL queries containing inner and outer join operations
5,832,480	M. A. Choi, N. Wacholder, R. J. Byrd, and Y. Ravin	Using canonical forms to develop a dictionary of names in a text
5,832,482	B. C. Liang, M. S. Chen, and P. S. Yu	Method for mining causality rules with applications to electronic commerce
5,832,533	B. Olsson, F. G. Gustavson, M. A. Johnson, and R. C. Agarwal	Method and system for addressing registers in a data processing unit in an indexed addressing mode
5,833,883	A. Afzali, J. D. Gelorme, and L. L. Kosbar	Cross-linked biobased materials and uses thereof
5,834,374	C. Cabral, F. M. d'Heurle, L. Clevenger, and Q. Hong	Method for controlling tensile and compressive stresses and mechanical problems in thin films on substrates
5,834,405	B. T. Ahn, E. O'Sullivan, E. A. Giess, E. I. Cooper, J. M. Roldan, L. T. Romankiw, and R. B. Beyers	Superconducting multilayer ceramic substrate
5,834,537	A. Viehbeck, J. C. Hedrick, J. T. Gotro, K. Papathomas, N. M. Patel, and W. D. Joseph	Homo-, co- or multicomponent thermoplastic polymer dispersed in a thermoset resin
5,834,642	B. Jurjevic, C. Huynh, D. Decain, D. P. Nadeau, and M. A. Taubenblatt	Downstream monitor for CMP brush cleaners
5,834,818	C. L. Bertin, J. A. Mandelman, J. M. Leas, and K. E. Beilstein	Structure for making sub-lithographic images by the intersection of two spacers
5,834,829	B. Dinkel, E. N. Levine, and P. Lee	Energy relieving crack stop
5,835,024	A. R. Williams, E. Upfal, H. T. Olnowich, J. Bruck, J. W. Feeney, and M. H. Fisher	Multi-stage interconnection network with selectable function switching apparatus
5,835,079	J. M. Shieh	Virtual pointing device for touchscreens
5,835,088	W. Jaaskelainen, Jr.	Method and apparatus for providing programmable window-to-window focus change within a data processing system using a graphical user interface
5,835,091	J. H. Bailey, M. A. Buttigieg, R. O. Turek, and S. E. Taylor	Manipulating and displaying a plurality of views in a graphical user interface
5,835,309	Z. E. Boutaghou	Pivot bearing
5,835,311	B. E. Schultz, J. T. Coffey, L. Berg, and W. W. Brooks, Jr.	Shock-resistant data storage system actuator assembly
5,835,377	R. R. Bush	Method and system for optimized material movement within a computer based manufacturing system utilizing global positioning systems